

# SafeFLASH NAND Driver for Samsung K9F1G08 User Guide

Version 1.00

For use with SafeFLASH NAND Driver for Samsung  
K9F1G08 versions 1.01 and above

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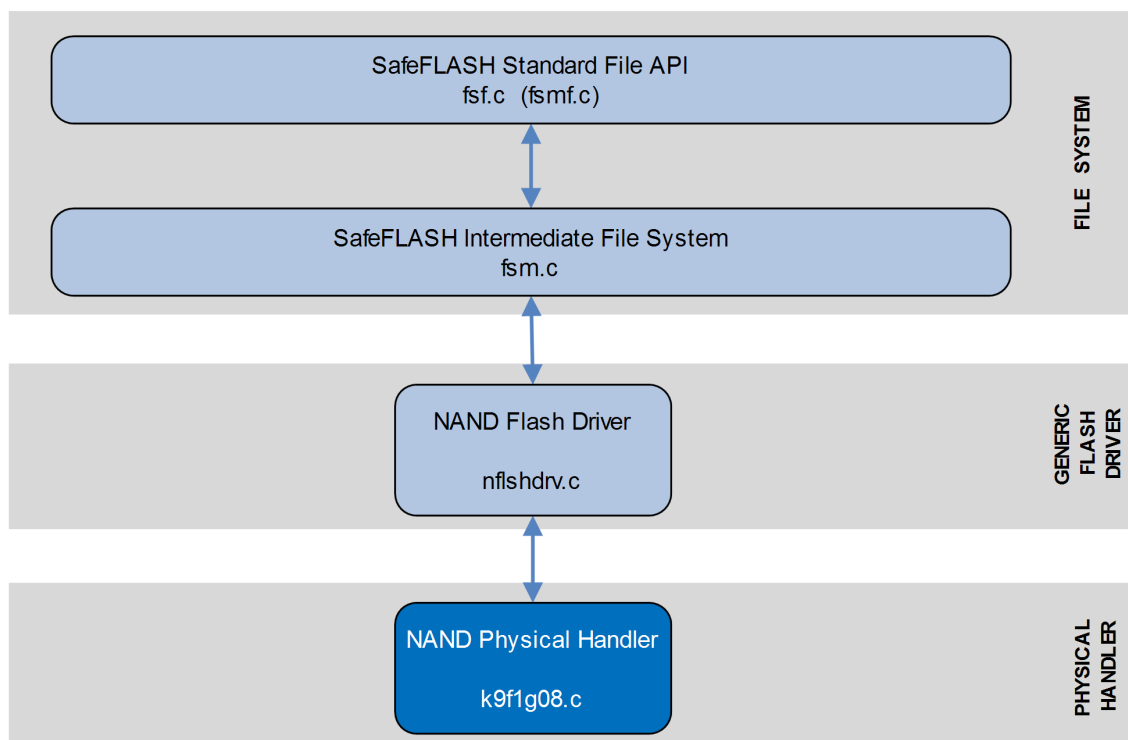
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# 1 System Overview

## 1.1 Introduction

This guide is for those who want to implement a SafeFLASH NAND driver for Samsung K9F1G08 and similar flash devices. This is for use with HCC's SafeFLASH file system. The serial flash appears in the micro-controller's address space and can be accessed in standard RAM fashion.

The following diagram illustrates the structure of the file system software:



The main SafeFLASH package provides the file API and intermediate file system. This is described in the [HCC SafeFLASH File System User Guide](#). This design is highly portable and gives excellent performance. The basic device architecture includes a high level driver for each general media type that shares some common properties. This driver handles issues of FAT maintenance, wear leveling, and so on.

Also shown in this diagram are the following:

- The NAND driver – the generic device driver for NAND flash, provided by the base NAND package. This is described in the [HCC SafeFLASH File System NAND Drive User Guide](#).
- The NAND physical handler – provided by this module, this performs the translation between the driver and the physical flash hardware.

**Note:** HCC Embedded offers hardware and firmware development consultancy to assist developers with the implementation of flash file systems.

## 1.2 Feature Check

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The main features of the module are the following:

- Conforms to the HCC Advanced Embedded Framework.
- Designed for integration with both RTOS and non-RTOS based systems.
- Supports Samsung K9F1G08 NAND flash and is easily configurable for similar NAND flash devices.
- Supports static and dynamic wear leveling.
- Provides bad block management.

## 1.3 Device Description

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This table summarizes the properties of the device:

|                    | <b>K9F1G08</b>                                  |
|--------------------|---|
| <b>Size</b>        | 1 Gb  |
| <b>Width</b>       | x8 bit  |
| <b>Page size</b>   | 2kB + 64 bytes                                  |
| <b>Block size</b>  | 128K + 4K bytes                                 |
| <b>Device size</b> | (2K+64)B x 64Pages x 1,024 Blocks = 1,056 Mbits |
| <b>ECC</b>         | 1bit, 512Byte ECC                               |

## 1.4 Fail-safety

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This driver is designed as part of HCC's SafeFLASH file system. SafeFLASH guarantees a defined level of fail-safety (see the [SafeFLASH File System User Guide](#)). For the system to be able to guarantee fail-safety, each component must provide a defined quality of service.

For this driver the following must be guaranteed to ensure the system is fail-safe:

- All write operations must be committed to flash in the sequence in which they are provided to the driver.
- Any write operation that fails must return an error.
- Any erase operation that fails must return an error.
- The system must ensure that there is at most one partially complete write or erase operation. At this point the file system should be restarted so that it can be recovered.

To achieve this in practice, the target hardware should ensure that in the event of a falling voltage the system resets or signals when the level approaches the specified programming level of the flash chip and inhibits further flash access.

There are other ways to manage this, for instance by adding a capacitance to ensure power is still available to complete an operation after a hardware error or reset condition is detected.

By using these techniques, the system can guarantee correct operation even after an unexpected system reset.

## 1.5 Packages and Documents

### Packages

The table below lists the packages that you need in order to use this module:

| Package                               | Description   |
|---------------------------------------|---|
| <code>hcc_base_doc</code>             | This contains the two guides that will help you get started.                      |
| <code>fs_safe</code>                  | The SafeFLASH base package.   |
| <code>fs_safe_nand</code>             | The SafeFLASH NAND flash driver.  |
| <code>fs_safe_nand_drv_k9f1g08</code> | The SafeFLASH NAND driver for Samsung K9F1G08 package described in this document. |

### Documents

For an overview of HCC file systems and guidance on choosing a file system, see [Product Information](#) on the main HCC website.

Readers should note the points in the [HCC Documentation Guidelines](#) on the HCC documentation website.

#### HCC Firmware Quick Start Guide

This document describes how to install packages provided by HCC in the target development environment. Also follow the *Quick Start Guide* when HCC provides package updates.

#### HCC Source Tree Guide

This document describes the HCC source tree. It gives an overview of the system to make clear the logic behind its organization.

#### HCC SafeFLASH File System User Guide

This document describes the base SafeFLASH System.

#### HCC SafeFLASH File System NAND Drive User Guide

This document describes the SafeFLASH NAND driver that is used with the Samsung K9F1G08 driver.

#### HCC SafeFLASH NAND Driver for Samsung K9F1G08 User Guide

This is this document.

## 2 Source File List

The following sections describe all the source code files included in the system. These files follow the HCC Embedded standard source tree system, described in the [HCC Source Tree Guide](#). All references to file pathnames refer to locations within this standard source tree, not within the package you initially receive.

**Note:** Do not modify any files except the PSP files.

### 2.1 System Files

These files are in the directory `src/safe-flash/nand/phy/samsung`. These files should only be modified by HCC.

| File                   | Description         |
|------------------------|---------------------|
| <code>k9f1g08.c</code> | Driver source code. |
| <code>k9f1g08.h</code> | Driver header file. |

### 2.2 Platform Support Package (PSP) Files

These files in the directory `src/psp/target/nand_k9f1g08` define the `psp_nand_k9f1g08_init()` function that configures the hardware.

**Note:** These are PSP implementations for the specific microcontroller and board; you may need to modify these to work with a different microcontroller and/or development board.

| File                            | Description                        |
|---------------------------------|------------------------------------|
| <code>psp_nand_k9f1g08.c</code> | Low level initialization function. |
| <code>psp_nand_k9f1g08.h</code> | Header file.                       |

### 2.3 Version File

The file `src/version/ver_nand_k9f1g08.h` contains the version number of this module. This version number is checked by all modules that use this module to ensure system consistency over upgrades.

## 3 PSP Porting

The Platform Support Package (PSP) is designed to hold all platform-specific functionality, either because it relies on specific features of a target system, or because this provides the most efficient or flexible solution for the developer.

The files `psp_nand_k9f1g08.c` `psp_nand_k9f1g08.h` define the `psp_nand_k9f1g08_init()` function that configures the hardware. Modify these files as required for your hardware.

### 3.1 psp\_nand\_k9f1g08\_init

This low level init function is called from the `mlayer_lowinit` function once.

#### Format

```
void psp_nand_k9f1g08_init ( void )
```

#### Arguments

None.

#### Return Values

| Return value | Description           |
|--------------|-----------------------|
| 0            | Successful execution. |
| 1            | Operation failed.     |