



SafeFLASH NAND Driver for Samsung K9F1G08 User Guide

Version 2.00

For use with SafeFLASH NAND Driver for Samsung
K9F1G08 versions 1.01 and above

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1. System Overview

This chapter contains the fundamental information for this module.

The component sections are as follows:

- [Introduction](#) - describes the main elements of the module.
- [Feature Check](#) - summarizes the main features of the module as bullet points.
- [Fail-safety](#) - defines fail-safety and describes the quality of service that SafeFLASH provides.
- [Device Description](#) - summarizes the properties of the supported device.
- [Packages and Documents](#) - the *Packages* section lists the packages that you need in order to use this module. The *Documents* section lists the relevant user guides.
- [Change History](#) - lists the earlier versions of this manual, giving the software version that each manual describes.

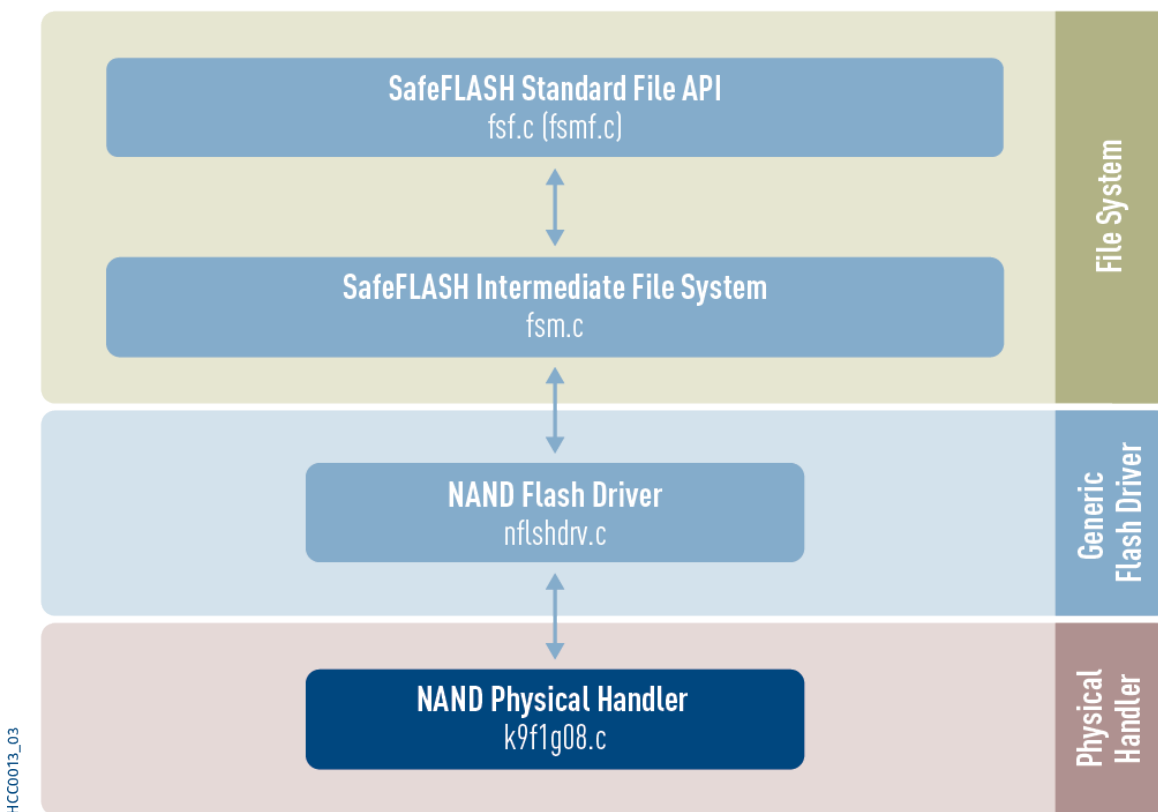
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1.1. Introduction

This guide is for those who want to implement a SafeFLASH NAND driver for Samsung K9F1G08 and similar flash devices. This is for use with HCC's SafeFLASH file system. The serial flash appears in the micro-controller's address space and can be accessed in standard RAM fashion.

The following diagram illustrates the structure of the file system software:



The main SafeFLASH package provides the file API and intermediate file system. This is described in the [HCC SafeFLASH File System User Guide](#). This design is highly portable and gives excellent performance. The basic device architecture includes a high level driver for each general media type that shares some common properties. This driver handles issues of FAT maintenance, wear leveling, and so on.

Also shown in this diagram are the following:

- The NAND driver - the generic device driver for NAND flash, provided by the base NAND package. This is described in the [HCC SafeFLASH File System NAND Drive User Guide](#).
- The NAND physical handler - provided by this module, this performs the translation between the driver and the physical flash hardware.

Note: HCC Embedded offers hardware and firmware development consultancy to assist developers with the implementation of flash file systems.

1.2. Feature Check

The main features of the module are the following:

- Conforms to the HCC Advanced Embedded Framework.
- Designed for integration with both RTOS and non-RTOS based systems.
- Supports Samsung K9F1G08 NAND flash and is easily configurable for similar NAND flash devices.
- Supports static and dynamic wear leveling.
- Provides bad block management.

1.3. Device Description

This table summarizes the properties of the device:

Property	K9F1G08
Size	1 Gb
Width	x8 bit
Page size	2kB + 64 bytes
Block size	128K + 4K bytes
Device size	(2K+64)B x 64 Pages x 1,024 Blocks = 1,056 Mbits
ECC	1bit, 512 byte ECC

1.4. Fail-safety

This SafeFLASH NAND driver for Samsung K9F1G08 is designed as part of HCC's SafeFLASH file system. SafeFLASH guarantees a defined level of fail-safety (see the [SafeFLASH File System User Guide](#)). For the system to be able to guarantee fail-safety, each component must provide a defined quality of service.

For this driver the following must be guaranteed to ensure the system is fail-safe:

- All write operations must be committed to flash in the sequence in which they are provided to the driver.
- Any write operation that fails must return an error.
- Any erase operation that fails must return an error.
- The system must ensure that there is at most one partially complete write or erase operation. At this point the file system should be restarted so that it can be recovered.

To achieve this, the hardware should ensure that, in the event of a falling voltage approaching the specified minimum programming level of the flash, the system either resets or provides a signal to the software to block access to the flash.

An alternative solution is to add capacitance to the design. This must provide sufficient power that, after a hardware error or reset condition is detected, the active operation on the flash can be completed.

Only by using one of these techniques can the system guarantee correct operation even after an unexpected system reset.

1.5. Packages and Documents

Packages

The table below lists the packages that you need in order to use this module:

Package	Description
<code>hcc_base_doc</code>	This contains the two guides that will help you get started.
<code>fs_safe_nand</code>	The SafeFLASH NAND flash driver.
<code>fs_safe_nand_drv_k9f1g08</code>	The SafeFLASH NAND driver for Samsung K9F1G08 package described in this document.

Documents

For an overview of HCC file systems and guidance on choosing a file system, see [Product Information](#) on the main HCC website.

Readers should note the points in the [HCC Documentation Guidelines](#) on the HCC documentation website.

HCC Firmware Quick Start Guide

This document describes how to install packages provided by HCC in the target development environment. Also follow the *Quick Start Guide* when HCC provides package updates.

HCC Source Tree Guide

This document describes the HCC source tree. It gives an overview of the system to make clear the logic behind its organization.

HCC SafeFLASH File System User Guide

This document describes the base SafeFLASH System.

HCC SafeFLASH File System NAND Drive User Guide

This document describes the SafeFLASH NAND driver that is used with the Samsung K9F1G08 driver.

HCC SafeFLASH NAND Driver for Samsung K9F1G08 User Guide

This is this document.

1.6. Change History

This section describes past changes to this manual.

- To view or download earlier manuals, see [File System PDFs](#).
- For the history of changes made to the package code itself, see [History: fs_safe_nand_drv_k9f1g08](#).

The current version of this manual is 2.00. The full list of versions is as follows:

Manual version	Date	Software version	Reason for change
2.00	2020-02-03	1.01	New template.
1.40	2019-10-11	1.01	Added to <i>PSP Files</i> section in <i>Source Files</i> .
1.30	2018-07-13	1.01	Added second note to <i>PSP Files</i> section in <i>Source Files</i> .
1.20	2017-08-31	1.01	Corrected <i>Packages</i> list.
1.10	2017-06-26	1.01	New <i>Change History</i> format.
1.00	2017-04-24	1.01	First online version.

2. Source File List

The following sections describe all the source code files included in the system. These files follow the HCC Embedded standard source tree system, described in the [HCC Source Tree Guide](#). All references to file pathnames refer to locations within this standard source tree, not within the package you initially receive.

Note: Do not modify any files except the PSP files.

System Files

These files are in the directory **src/safe-flash/nand/phy/samsung**. **These files should only be modified by HCC.**

File	Description
k9f1g08.c	Driver source code.
k9f1g08.h	Driver header file.

Version File

The file **src/version/ver_nand_k9f1g08.h** contains the version number of this module. This version number is checked by all modules that use this module to ensure system consistency over upgrades.

Platform Support Package (PSP) Files

There is one set of files, in the directory **psp_lpc1788**, for use with the LPC1788 Development Board.

The following files are in the directory **src/psp/target/nand_k9f1g08** and define the [psp_nand_k9f1g08_init\(\)](#) function that configures the hardware.

Note:

- These are PSP implementations for the specific microcontroller and board; you may need to modify these to work with a different microcontroller and/or development board. See [PSP Porting](#) for details.
- In the package these files are offset to avoid overwriting an existing implementation. Copy them to the root **hcc** directory for use.

The files are as follows:

File	Description
psp_nand_k9f1g08.c	Low level initialization function.
psp_nand_k9f1g08.h	Header file.

The PSP also has a version file, **ver_psp_nand_k9f1g08.h**.

3. PSP Porting

The Platform Support Package (PSP) is designed to hold all platform-specific functionality, either because it relies on specific features of a target system, or because this provides the most efficient or flexible solution for the developer.

The files **psp_nand_k9f1g08.c** **psp_nand_k9f1g08.h** define the [psp_nand_k9f1g08_init\(\)](#) function that configures the hardware. Modify these files as required for your hardware.

3.1. psp_nand_k9f1g08_init

This low level init function is called from the *m/layer* **lowinit** function once.

Format

```
void psp_nand_k9f1g08_init ( void )
```

Arguments

None.

Return Values

Return value	Description
0	Successful execution.
1	Operation failed.

4. Version

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