



# SafeFLASH NOR Driver for Macronix MX29GL128F User Guide

Version 2.00

For use with SafeFLASH NOR Driver for Macronix  
MX29GL128F versions 1.01 R2 and above

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# 1. System Overview

This chapter contains the fundamental information for this module.

The component sections are as follows:

- [Introduction](#) - describes the main elements of the module.
- [Feature Check](#) - summarizes the main features of the module as bullet points.
- [Fail-safety](#) - defines fail-safety and describes the quality of service that SafeFLASH provides.
- [Device Description](#) - summarizes the properties of the supported Macronix devices.
- [Packages and Documents](#) - the *Packages* section lists the packages that you need in order to use this module. The *Documents* section lists the relevant user guides.
- [Change History](#) - lists the earlier versions of this manual, giving the software version that each manual describes.

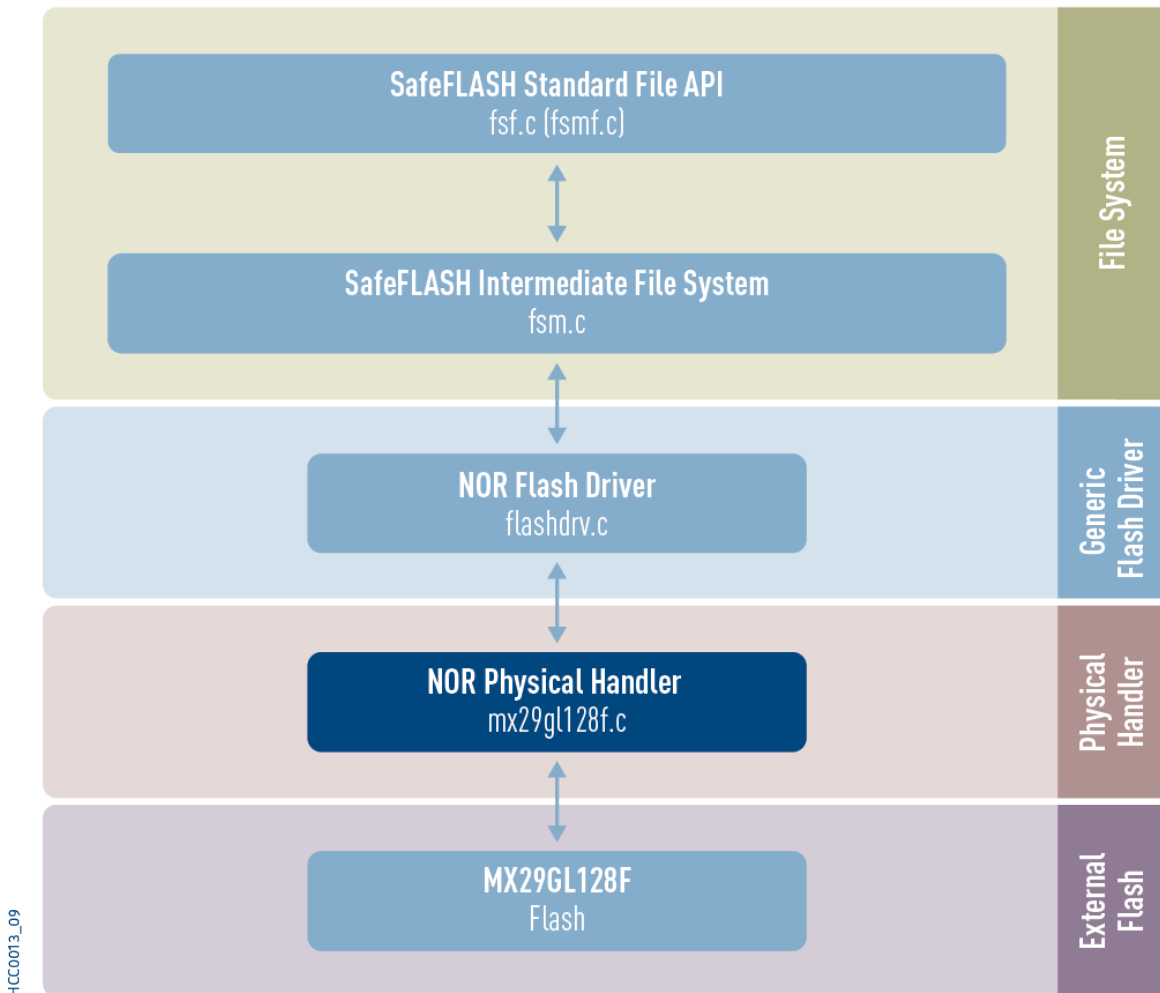
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## 1.1. Introduction

This guide is for those who want to implement a SafeFLASH NOR driver for Macronix MX29GL128F and similar devices from Macronix International Co. This is for use with HCC's SafeFLASH file system.

The following diagram shows the structure of the file system software:



This diagram shows:

- The main SafeFLASH package - this provides the file API and intermediate file system. This is described in the [HCC SafeFLASH File System User Guide](#).
- The NOR flash driver - the generic device driver for NOR flash, provided by the base NOR package. This driver handles issues of FAT maintenance, wear leveling, and so on. It is described in the [HCC SafeFLASH File System NOR Drive User Guide](#).
- The NOR physical handler - provided by this module, this performs the translation between the driver and the physical flash hardware.
- The flash device.

**Note:** HCC Embedded offers hardware and firmware development consultancy to assist developers with the implementation of flash file systems.

## 1.2. Feature Check

The main features of the module are the following:

- Conforms to the HCC Advanced Embedded Framework.
- Designed for integration with both RTOS and non-RTOS based systems.
- Supports Macronix MX29GL128F flash and is easily configurable for similar NOR flash parts.
- Supports static and dynamic wear leveling.
- Provides bad block management.
- A sample driver is available with a porting description.

## 1.3. Device Description

This driver is specifically tested and validated with the MX29GL128F device. In addition, note that:

- With minor modification, the driver should work with all MX29xyyyz devices: MX29GL512F, MX29GL512G, MX29GA512F, and MX29GL512E.

This table summarizes the properties of the devices:

Name	Description
<b>MX29</b>	Family prefix.
<b>GA, GL, VS</b>	GA = Standard Read Access GL = Page Mode Read Access VS = Burst mode read access.
<b>128/256/512</b>	Density 128Mb/256Mb/512Mb.

This table shows the density and bus width:

	MX29GL128F
<b>Density</b>	128Mb
<b>Bus Width</b>	x8 or x16

The devices also have:

- 64KW/128KB uniform sector architecture - 128 equal sectors.
- 16-byte/8-word page read buffer.
- 64-byte/32-word write buffer.
- Extra 128-word sector for security.

## 1.4. Fail-safety

This driver for MX29G128F NOR flash is designed as part of HCC's SafeFLASH file system. SafeFLASH guarantees a defined level of fail-safety (see the [SafeFLASH File System User Guide](#)). For the system to be able to guarantee fail-safety, each component must provide a defined quality of service.

For this driver the following must be guaranteed to ensure the system is fail-safe:

- All write operations must be committed to flash in the sequence in which they are provided to the driver.
- Any write operation that fails must return an error.
- Any erase operation that fails must return an error.
- The system must ensure that there is at most one partially complete write or erase operation. At this point the file system should be restarted so that it can be recovered.

To achieve this, the hardware should ensure that, in the event of a falling voltage approaching the specified minimum programming level of the flash, the system either resets or provides a signal to the software to block access to the flash.

An alternative solution is to add capacitance to the design. This must provide sufficient power that, after a hardware error or reset condition is detected, the active operation on the flash can be completed.

Only by using one of these techniques can the system guarantee correct operation even after an unexpected system reset.



## 1.5. Packages and Documents

### Packages

The table below lists the packages that you need in order to use this module:

Package	Description
<code>hcc_base_doc</code>	This contains the two guides that will help you get started.
<code>fs_safe_nor</code>	The SafeFLASH NOR flash driver.
<code>fs_safe_nor_drv_mx29gl128f</code>	The low level driver package described in this document.

### Documents

For an overview of HCC file systems and guidance on choosing a file system, see [Product Information](#) on the main HCC website.

Readers should note the points in the [HCC Documentation Guidelines](#) on the HCC documentation website.

#### HCC Firmware Quick Start Guide

This document describes how to install packages provided by HCC in the target development environment. Also follow the *Quick Start Guide* when HCC provides package updates.

#### HCC Source Tree Guide

This document describes the HCC source tree. It gives an overview of the system to make clear the logic behind its organization.

#### HCC SafeFLASH File System User Guide

This document describes the base SafeFLASH System.

#### HCC SafeFLASH File System NOR Drive User Guide

This document describes the SafeFLASH NOR generic driver.

#### HCC SafeFLASH NOR Driver for Macronix MX29GL128F User Guide

This is this document.



## 1.6. Change History

This section describes past changes to this manual.

- To download this manual or a PDF describing an [earlier software version, see File System PDFs](#).
- For the history of changes made to the package code itself, see [History: fs\\_safe\\_nor\\_drv\\_mx29gl128f](#).

The current version of this manual is 2.00. The full list of versions is as follows:

Manual version	Date	Software version	Reason for change
2.00	2020-02-04	1.01 R2	New template.
1.40	2019-01-23	1.02	Corrected diagram in <i>Introduction</i> .
1.30	2018-07-13	1.02	Added second note to <i>PSP Files</i> section in <i>Source Files</i> .
1.20	2017-08-31	1.02	Corrected <i>Packages</i> list.
1.10	2017-06-26	1.02	New <i>Change History</i> format.
1.00	2017-04-24	1.02	First online version.

## 2. Source File List

The following sections describe all the source code files included in the system. These files follow the HCC Embedded standard source tree system, described in the [HCC Source Tree Guide](#). All references to file pathnames refer to locations within this standard source tree, not within the package you initially receive.

**Note:** Do not modify any files except the configuration file and PSP files.

### Configuration File

The file `src/config/config_safe_nor_mx29gl128f.h` contains the configurable system parameters. Configure these as required. This is the only file in the module that you should modify. For details of the options, see [Configuration Options](#).

### System Files

These files are in the directory `src/safe-flash/nor/phy/macronix`. **These files should only be modified by HCC.**

File	Description
<code>mx29gl128f.c</code>	Driver source code.
<code>mx29gl128f.h</code>	Driver header file.

### Version File

The file `src/version/ver_safe_nor_drv_mx29gl128f.h` contains the version number of this module. This version number is checked by all modules that use this module to ensure system consistency over upgrades.

### Platform Support Package (PSP) Files

These files in the directory `src/psp/target/macronix` define the `psp_mx29gl128f_hw_init()` function that configures the hardware.

**Note:**

- These are PSP implementations for the specific micro-controller and board; you may need to modify these to work with a different micro-controller and/or development board; see [PSP Porting](#).
- In the package these files are offset to avoid overwriting an existing implementation. Copy them to the root `hcc` directory for use.

The files are as follows:

File	Description
<b>psp_mx29gl128f_hw.c</b>	Low level initialization function.
<b>psp_mx29gl128f_hw.h</b>	Header file.

The PSP also has a version file, **ver\_psp\_mx29gl128f\_hw.h**.

## 3. Configuration Options

Set the system configuration options in the file `src/config/config_safe_nor_mx29gl128f.h`. This section lists the options and their default values.

### **NOR\_MAN\_ID**

The identifier of the flash manufacturer. This is fixed at 0xC2 for this device, representing Macronix.

**Note:** The following three options are device ID words that represent this device type. Do not change any of these values unless you port this driver to support another similar chip.

### **NOR\_DEV\_ID**

The default is 0x227E.

### **NOR\_DEV\_ID2**

The default is 0x2221.

### **NOR\_DEV\_ID3**

The default is 0x2201.

**Note:** For full instructions on how to set up the following options, see the following sections in the *HCC SafeFLASH File System NOR Drive User Guide*:

- [Sectors and File Storage](#)
- [Physical Device Usage](#) - this describes all the block types in detail and gives examples.

### **NOR\_BLOCKSTART**

The block start. Blocks before the specified `NOR_BLOCKSTART` are not used. The default is 0.

### **NOR\_SECTORSIZE**

The logical sector size. The default is 4096.

### **NOR\_DESCSIZE**

The descriptor size. The default is ( 16 \* 1024 ).

### **NOR\_CACHEDESCSIZE**

The cache size. The default is ( 4 \* 1024 ).

### **NOR\_BLOCK\_SIZE**

The block size. The default is ( 128 \* 1024 ).

**NOR\_NUM\_BLOCKS**

The number of blocks used by the file system. The default is 128.

## 4. PSP Porting

The Platform Support Package (PSP) is designed to hold all platform-specific functionality, either because it relies on specific features of a target system, or because this provides the most efficient or flexible solution for the developer.

The files **psp\_mx29gl128f\_hw.c** and **psp\_mx29gl128f\_hw.h** define the [psp\\_mx29gl128f\\_hw\\_init\(\)](#) function that configures the hardware. Modify these files as required for your hardware.

## 4.1. psp\_mx29gl128f\_hw\_init

Use this function to initialize the device.

### Format

```
void psp_mx29gl128f_hw_init ( void )
```

### Arguments

None.

### Return Values

None.



## 5. Version

Version 2.00

For use with SafeFLASH NOR Driver for Macronix MX29GL128F versions 1.01 R2 and above